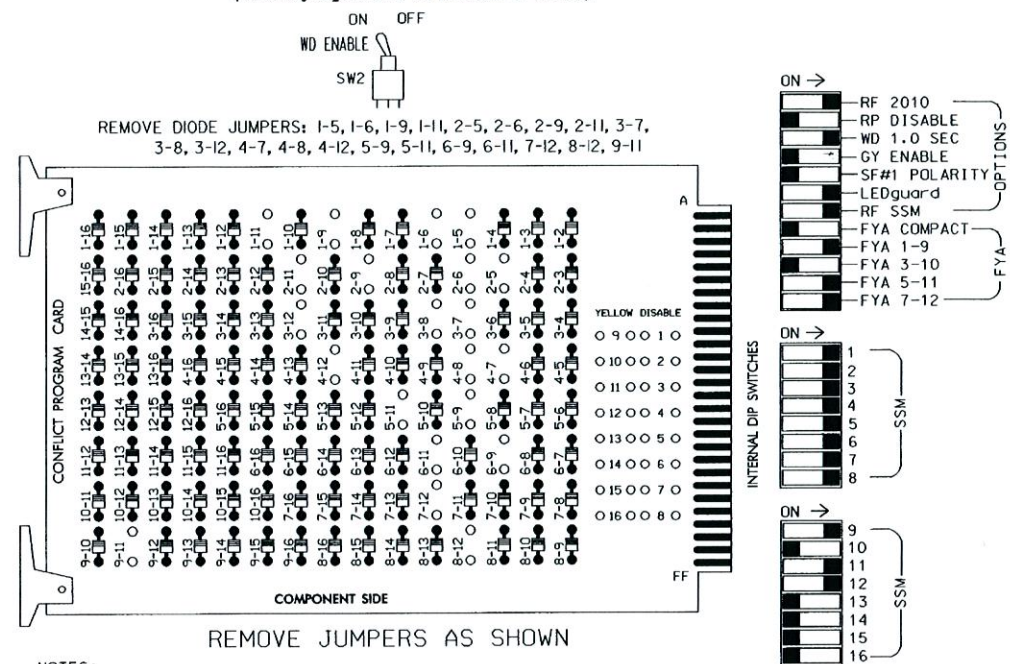


EDI MODEL 2010ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Make sure jumpers SEL2-SEL5 are present on the monitor board.

INPUT FILE POSITION LAYOUT (front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	1A	1B	2A	2B	3A	3B	4A	4B	SYS. DET. S1	SYS. DET. S2	SYS. DET. S3	SYS. DET. S4	SYS. DET. S5	SYS. DET. S6
L	NOT USED	NOT USED	2B	2B	NOT USED	NOT USED	SYS. DET. S4	SYS. DET. S5	SYS. DET. S6	SYS. DET. S7	SYS. DET. S8	SYS. DET. S9	SYS. DET. S10	SYS. DET. S11
U	5A	5B	6A	6B	7A	7B	8A	8B	SYS. DET. S5	SYS. DET. S6	SYS. DET. S7	SYS. DET. S8	SYS. DET. S9	SYS. DET. S10
L	NOT USED	NOT USED	6B	6B	NOT USED	SYS. DET. S3	SYS. DET. S4	SYS. DET. S5	SYS. DET. S6	SYS. DET. S7	SYS. DET. S8	SYS. DET. S9	SYS. DET. S10	SYS. DET. S11

EX.: 1A, 2A, ETC. = LOOP NO.'S

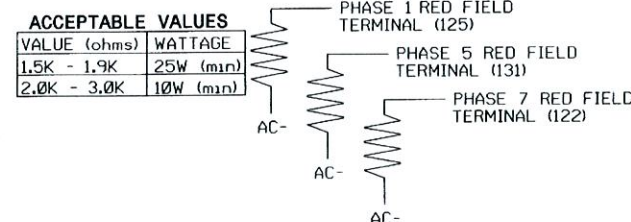
FS = FLASH SENSE
ST = STOP TIME

Wired Input - Do not populate slot with detector card

INPUT FILE POSITION LEGEND: J2L

FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL



NOTE: The purpose of these resistors is to load the channel red monitor inputs in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 10, 13, 14, 15, and 16 to load switch AC+ per the cabinet manufacturer's instructions.
- Program phase 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as a Wag Overlap.
- The cabinet and controller are part of the Wilmington Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....ECONOLITE 2070L
CABINET.....SAFETRAN 332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S12,S13
PHASES USED.....1,2,3,4,5,6,7,8
OVERLAP "A".....1+2
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....7+8

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y	-	-	10.5
	-	J4U	48	10	26	6	Y	Y	Y	-	3
1B	TB2-5,6	I2U	39	1	2	1	Y	Y	-	-	15
2A	TB2-9,10	I3U	63	25	32	2	Y	Y	-	-	-
2B	TB2-11,12	I3L	76	38	42	2	Y	Y	-	-	-
3A	TB4-5,6	I5U	58	20	3	3	Y	Y	-	-	3
3B	TB4-9,10	I6U	41	3	4	3	Y	Y	-	-	-
4A	TB6-1,2	I7U	65	27	34	4	Y	Y	-	-	-
* S4	TB6-3,4	I7L	78	40	44	SYS	-	-	-	-	-
* S1	TB6-9,10	I9U	60	22	11	SYS	-	-	-	-	-
* S2	TB6-11,12	I9L	62	24	13	SYS	-	-	-	-	-
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y	-	-	10.5
	-	I4U	47	9	22	2	Y	Y	Y	-	3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y	-	-	15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y	-	-	-
6B	TB3-11,12	J3L	77	39	46	6	Y	Y	-	-	-
7A ³	TB5-5,6	J5U	57	19	7	7	Y	Y	-	-	10.5
	-	I8U	49	11	24	4	Y	Y	-	-	-
8A	TB5-9,10	J6U	42	4	8	8	Y	Y	-	-	-
* S3	TB5-11,12	J6L	46	8	18	SYS	-	-	-	-	-
* S5	TB7-9,10	J9U	59	21	15	SYS	-	-	-	-	-
* S6	TB7-11,12	J9L	61	23	17	SYS	-	-	-	-	-

* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.

- Add jumper from I1W to J4W, on rear of input file.
- Add jumper from J1W to I4W, on rear of input file.
- Add jumper from J5W to I8W, on rear of input file.

Important!!

Remove existing jumpers from TB2-5 to TB2-7, from TB2-6 to TB2-8, from TB5-9 to TB5-11, and from TB5-10 to TB5-12.



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NCELS FIRM LICENSE NO. C-2522

Project #: 09-225

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	9	10	11	12	13	14
SIGNAL HEAD NO.	11	82	21,22	NU	22	31,32	41,42	NU	42	51	61,62	NU	62	71	81,82	NU	11	NU
RED	*	128			101	*	134	*	107									
YELLOW		129			102		135		108									
GREEN		130			103		136		109									
RED ARROW					116									A121		A114	A101	
YELLOW ARROW	126			117	117		132		123					A122		A115	A102	
FLASHING YELLOW ARROW														A123		A116	A103	
GREEN ARROW	127	127		118	118		133	133	124	124								

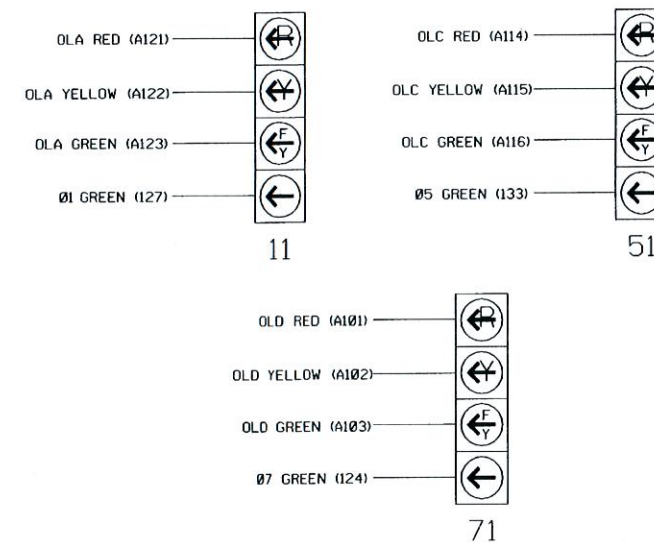
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE: The sequence display for these signals requires special logic programming. See sheet 2 of 2 for programming instructions.

NC Dept of Transportation
Division of Highways

Final Drawing Date 5/31/11

INNOCENT UMOCYRIKE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 03-0049
DESIGNED: May 2011
SEALED: May 19, 2011
REVISED:

Signal Upgrade - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR



US 17 Bus. (Market Street)
at
SR 1175 (Kerr Avenue)

Division 3 New Hanover County Wilmington

PLAN DATE: May 2011

REVIEWED BY:

PREPARED BY: R. Hinchey

REVIEWED BY:

REVISIONS

INIT. DATE

DATE

SIG. INVENTORY NO. 03-0049



5-19-11